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S13 8	33	S136 and S137	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:01
S13 9	8981	first clock signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:01
S14 0	8772	second clock signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:01
S14 1	1876	first error signal\$1 or composite error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:02
S14 2	7246	S139 and S140	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:02
S14 3	39	S142 and S141	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:02
S14 4	1	S143 and S138	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:03
S14 5	8	S138 and (S141 or S142)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 16:30
S14 6	1	("6300807").PN.	US-PGPUB; USPAT	OR	OFF	2005/07/06 18:17
S14 7	1	("5644743").PN.	US-PGPUB; USPAT	OR	OFF	2005/07/06 18:17

S14 8	2	((("5983383") or ("20010025358"))).PN.	US-PGPUB; USPAT	OR	OFF	2005/07/07 13:49
S14 9	636	clock distribution circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:04
S15 0	1358	distribut\$3 and (internal clock signal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:04
S15 1	9073	first clock signal\$1 or first clock circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:05
S15 2	8914	second clock signal\$1 or second clock circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:05
S15 3	33624	phase detector\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:05
S15 4	7308	S151 and S152	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:05
S15 5	573	S153 and S154	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:05
S15 6	1940	S149 or S150	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:05

S15 7	28	S155 and S156	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:06
S15 8	163	clock error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:06
S16 1	113818	error signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:06
S16 2	7	S157 and S161	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:07
S16 3	1502205	integrated circuit or IC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:07
S16 4	5	S163 and S162	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/07 15:31
S16 5	1	("6801989").PN.	US-PGPUB; USPAT	OR	OFF	2005/07/07 16:48
S16 6	1	("5552750").PN.	US-PGPUB; USPAT	OR	OFF	2005/07/07 16:48

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	("6539367").PN.	US-PGPUB; USPAT	OR	OFF	2005/07/08 10:28
S130	15754	synchroniz\$3 and (phase\$1 with clock signal\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/08 09:17
S131	113768	error signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 14:59
S132	3000	S130 and S131	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 14:59
S133	6143	phase differen\$3 with clock signal\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 14:59
S134	609	S132 and S133	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:00
S135	6177	generat\$3 and (clock signal\$1 with integrated circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:00
S136	62	S135 and S134	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:00
S137	2155	distribut\$3 and (clock signal\$1 with integrated circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/06 15:01